

ABSTRACT

A configuration control circuit (400) allows a PLD to be quickly re-configured to implement different functions without requiring any configuration memory cells. The control circuit (400) includes a first input (IN1) connected to a first hardwired configuration bit (HCB1), a second input (IN2) connected to a second hardwired configuration bit (HCB2), an output (OUT) connected to one or more of the PLD's configurable elements (110), and a select circuit (402) to selectively connect either the first input (IN1) or the second input (IN2) to the output (OUT) in response to a select signal (SEL).